

Application No. 09/811,528
Amendment dated: December 7, 2004

Amendments to the Abstract:

Please replace the Abstract presently on files with the following replacement Abstract.

A method and system for synchronizing a receiver's clock to the clock of a transmitter is disclosed herein. The disclosed system employs a digital clock synthesizer that uses patterns superimposed upon a receiver oscillator to synthesize a clock rate that approximates the clock rate of the transmitter. The pattern superimposed upon the receiver oscillator can be varied to allow for tracking of the variation in the transmitter clock.